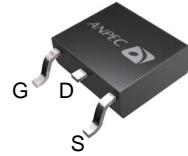


P-Channel Enhancement Mode MOSFET

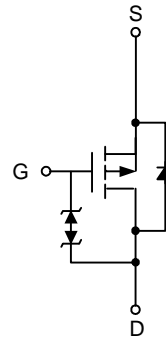
Features

- -40V/-25A,
 $R_{DS(ON)}=33m\Omega$ (typ.) @ $V_{GS}=-10V$
 $R_{DS(ON)}=47m\Omega$ (typ.) @ $V_{GS}=-4.5V$
- Super High Dense Cell Design
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

Pin Description



Top View of TO-252



P-Channel MOSFET

Application

- Inverter application in LCD Monitor/TV

Ordering and Marking Information

<p>APM4050AP □□-□□□</p> <div style="margin-left: 20px;"> <p>└─ Assembly Material</p> <p>└─ Handling Code</p> <p>└─ Temperature Range</p> <p>└─ Package Code</p> </div>	<p>Package Code U : TO-252</p> <p>Operating Junction Temperature Range C : -55 to 150 °C</p> <p>Handling Code TR : Tape & Reel</p> <p>Assembly Material G : Halogen and Lead Free Device</p>
<p>APM4050AP U: APM4050A XXXXX</p>	<p>XXXXX - Date Code</p>

Note: ANPEC lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. ANPEC lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020C for MSL classification at lead-free peak reflow temperature. ANPEC defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

ANPEC reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_A=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage	-40	V	
V_{GSS}	Gate-Source Voltage	± 20		
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$ -10	A	
Mounted on Large Heat Sink				
I_{DP}	300 μs Pulse Drain Current Tested	$T_C=25^\circ\text{C}$	-50	A
		$T_C=100^\circ\text{C}$	-40	
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	-25	A
		$T_C=100^\circ\text{C}$	-16	
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	50	W
		$T_C=100^\circ\text{C}$	20	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	2.5	$^\circ\text{C}/\text{W}$	
Mounted on PCB of 1in² Pad Area				
I_{DP}	300 μs Pulse Drain Current Tested	$T_A=25^\circ\text{C}$	-22	A
		$T_A=100^\circ\text{C}$	-14	
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	-5.5	A
		$T_A=100^\circ\text{C}$	-3.5	
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	2.5	W
		$T_A=100^\circ\text{C}$	1	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	50	$^\circ\text{C}/\text{W}$	
Mounted on PCB of Minimum Footprint				
I_{DP}	300 μs Pulse Drain Current Tested	$T_A=25^\circ\text{C}$	-18	A
		$T_A=100^\circ\text{C}$	-12	
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	-4.5	A
		$T_A=100^\circ\text{C}$	-3	
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	1.6	W
		$T_A=100^\circ\text{C}$	0.6	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	75	$^\circ\text{C}/\text{W}$	

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

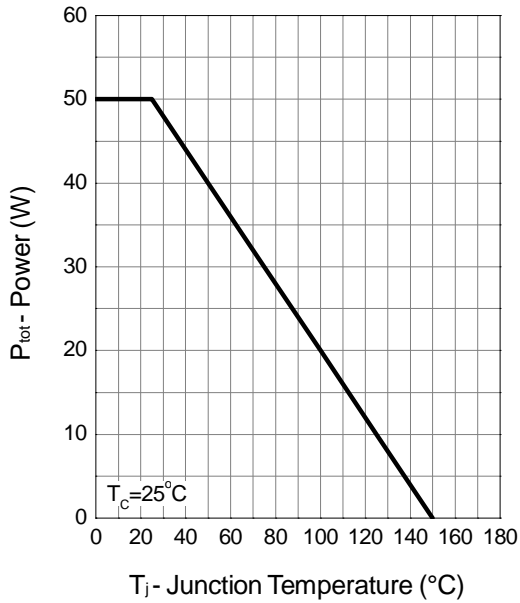
Symbol	Parameter	Test Conditions	APM4050APU			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=-250\mu A$	-40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-32V, V_{GS}=0V$	-	-	-1	μA
		$T_J=85^\circ C$	-	-	-30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-1.3	-1.8	-2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 16V, V_{DS}=0V$	-	-	± 10	μA
$R_{DS(ON)}^a$	Drain-Source On-state Resistance	$V_{GS}=-10V, I_{DS}=-12A$	-	33	45	$m\Omega$
		$V_{GS}=-4.5V, I_{DS}=-5A$	-	47	68	
Diode Characteristics						
V_{SD}^a	Diode Forward Voltage	$I_{SD}=-10A, V_{GS}=0V$	-	-0.8	-1.1	V
t_{rr}	Reverse Recovery Time	$I_{DS}=-12A, di_{SD}/dt=100A/\mu s$	-	15	-	ns
Q_{rr}	Reverse Recovery Charge		-	7	-	nC
Dynamic Characteristics^b						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	9.5	-	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=-20V,$ Frequency=1.0MHz	-	1080	-	pF
C_{oss}	Output Capacitance		-	120	-	
C_{riss}	Reverse Transfer Capacitance		-	70	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=-20V, R_L=20\Omega,$ $I_{DS}=-1A, V_{GEN}=-10V,$ $R_G=6\Omega$	-	11	21	ns
T_r	Turn-on Rise Time		-	9	17	
$t_{d(OFF)}$	Turn-off Delay Time		-	50	91	
T_f	Turn-off Fall Time		-	27	50	
Gate Charge Characteristics^b						
Q_g	Total Gate Charge	$V_{DS}=-20V, V_{GS}=-10V,$ $I_{DS}=-12A$	-	18	25	nC
Q_{gs}	Gate-Source Charge		-	2.5	-	
Q_{gd}	Gate-Drain Charge		-	4.5	-	

Note a : Pulse test ; pulse width $\leq 300ms$, duty cycle $\leq 2\%$.

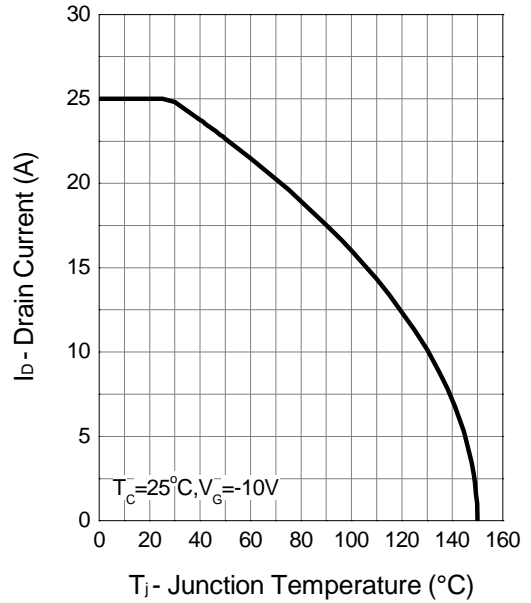
Note b : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

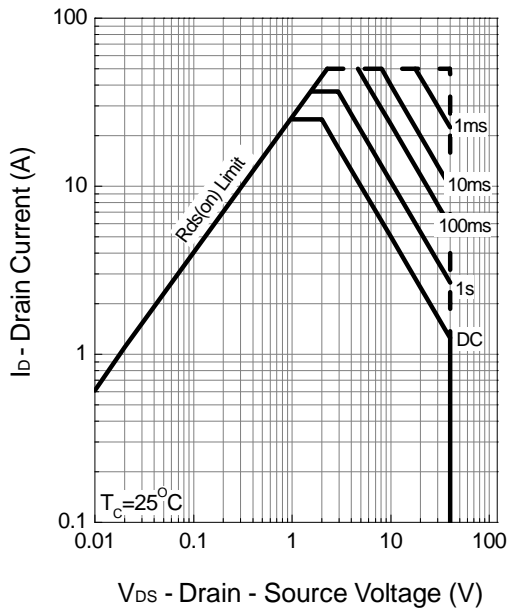
Power Dissipation



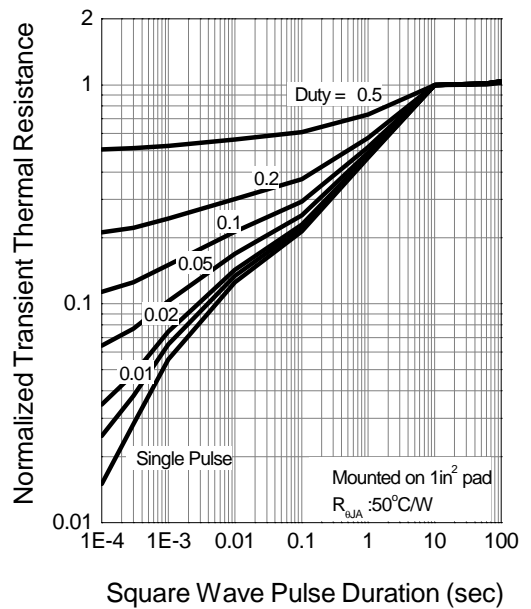
Drain Current



Safe Operation Area

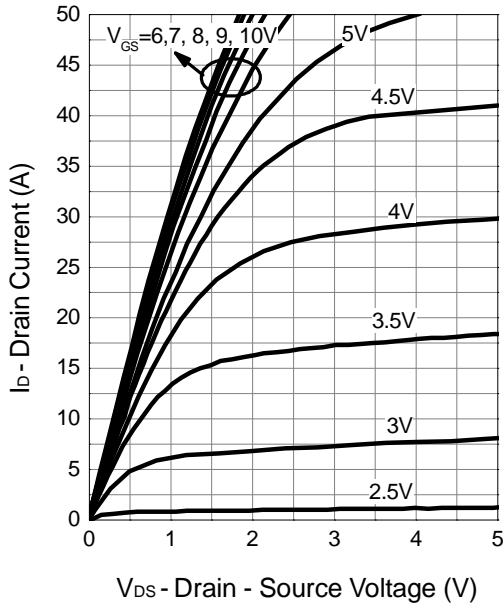


Thermal Transient Impedance

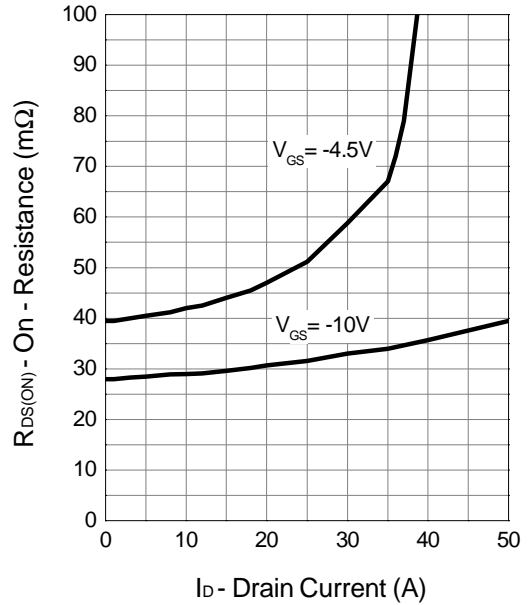


Typical Operating Characteristics (Cont.)

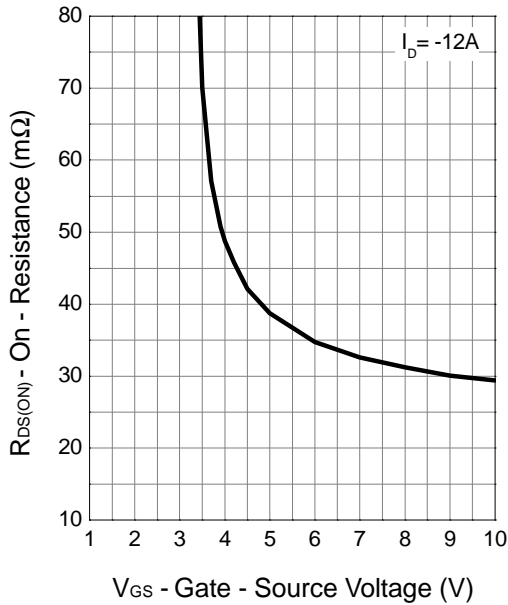
Output Characteristics



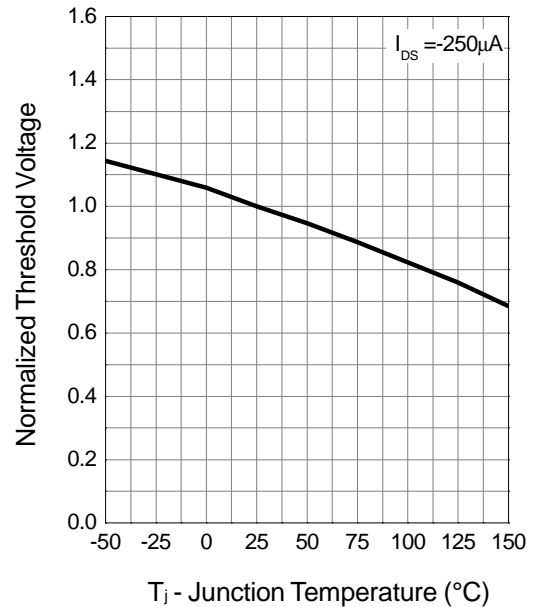
Drain-Source On Resistance



Drain-Source On Resistance

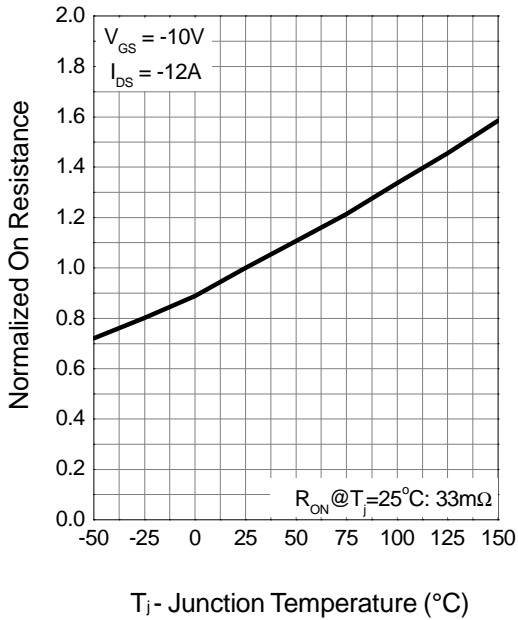


Gate Threshold Voltage

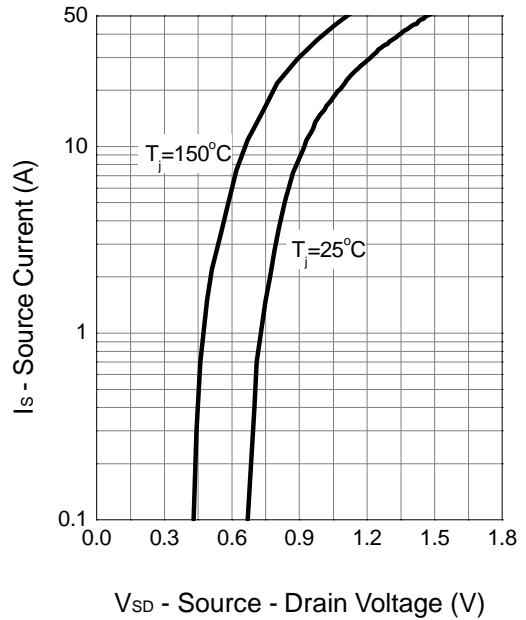


Typical Operating Characteristics (Cont.)

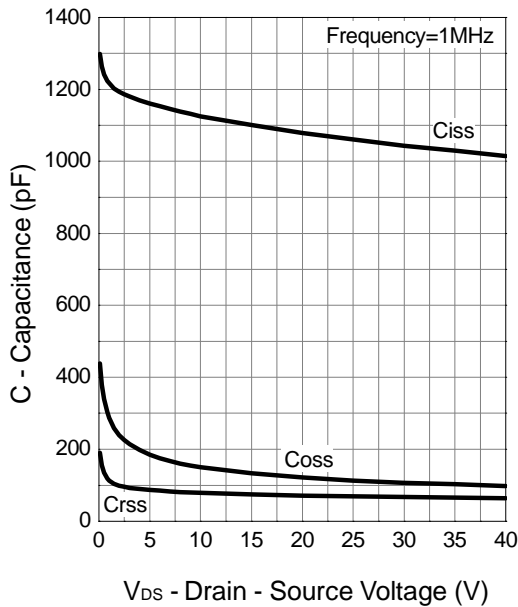
Drain-Source On Resistance



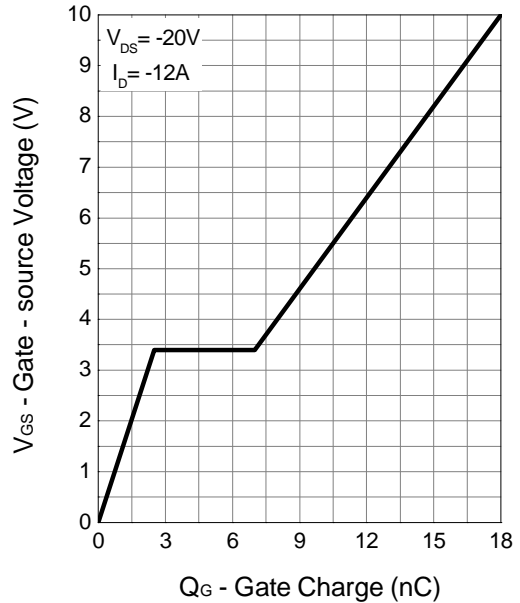
Source-Drain Diode Forward



Capacitance

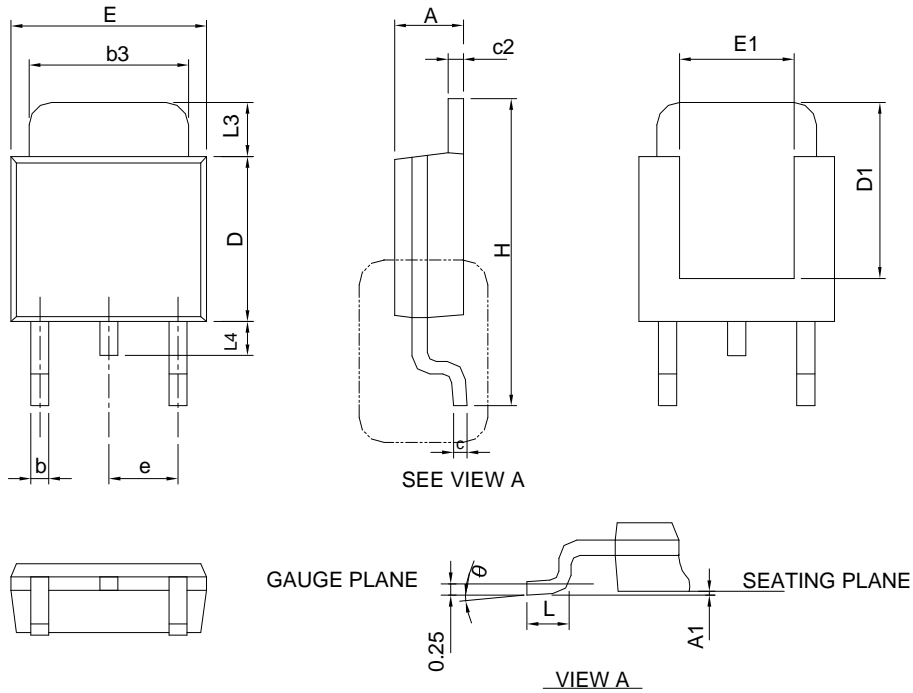


Gate Charge



Package Information

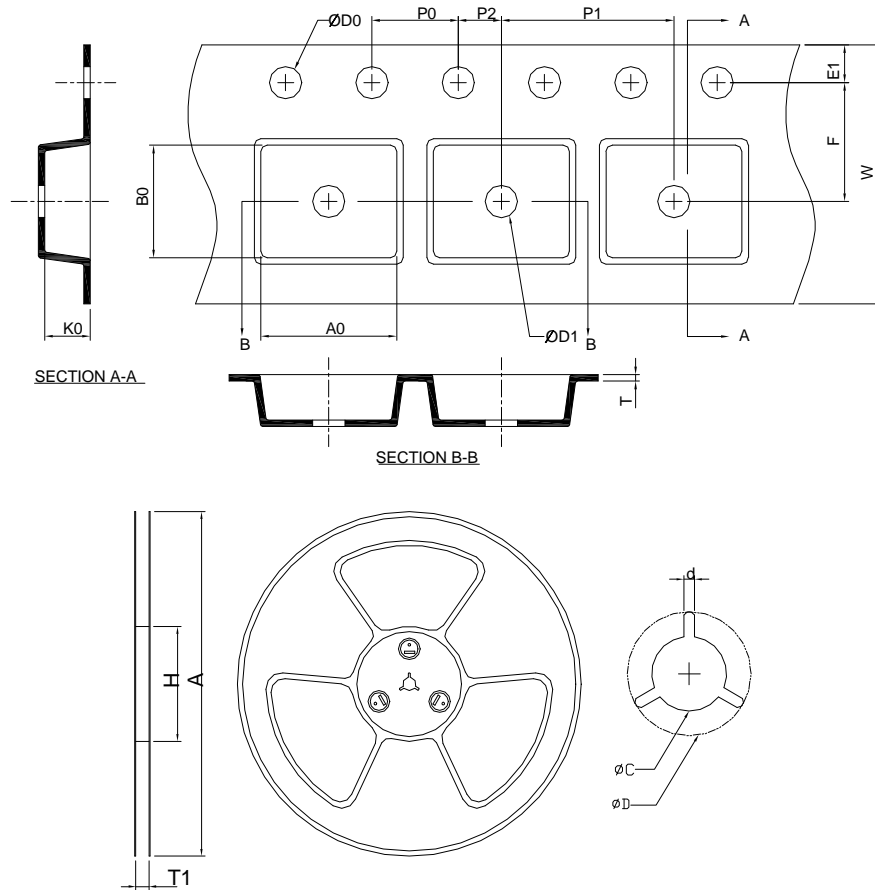
TO-252



SYMBOL	TO-252			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1		0.13		0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4		1.02		0.040
θ	0°	8°	0°	8°

Note : Follow JEDEC TO-252 .

Carrier Tape & Reel Dimensions



Application	A	H	T1	C	d	D	W	E1	F
TO-252	330.0 ±0.00	50 MIN.	16.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	16.0 ±0.30	1.75 ±0.10	7.50 ±0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 ±0.10	8.0 ±0.10	2.0 ±0.05	1.5+0.10 -0.00	1.5 MIN.	0.6+0.00 -0.40	6.80 ±0.20	10.40 ±0.20	2.50 ±0.20

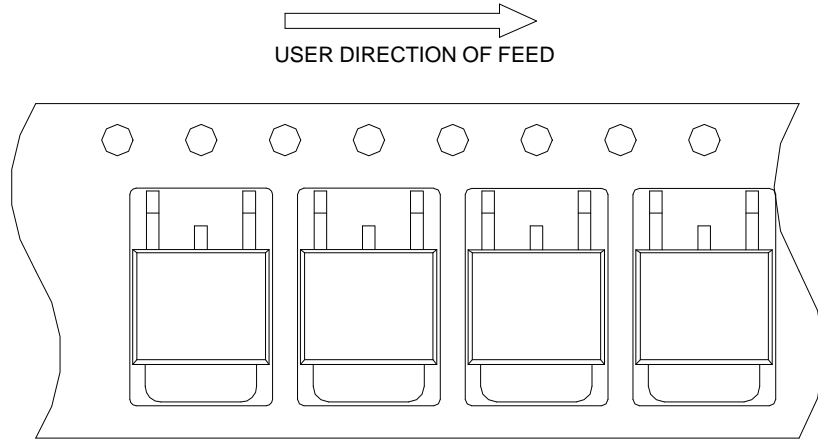
(mm)

Devices Per Unit

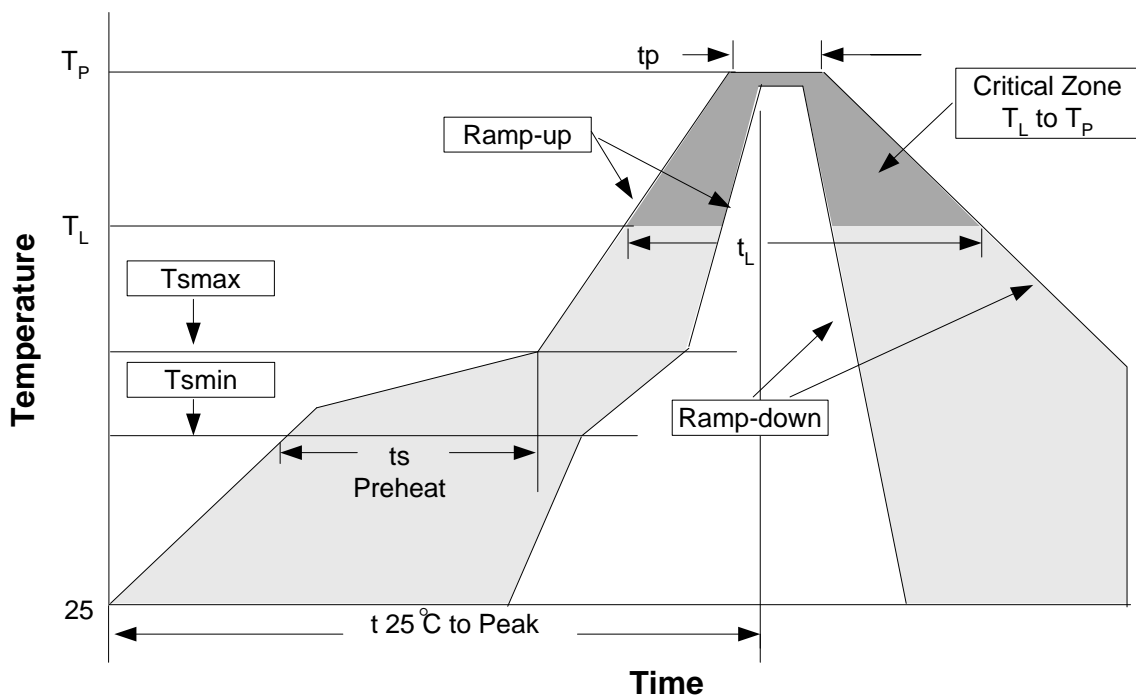
Package Type	Unit	Quantity
TO-252	Tape & Reel	2500

Taping Direction Information

TO-252



Reflow Condition (IR/Convection or VPR Reflow)



Reliability Test Program

Test item	Method	Description
SOLDERABILITY	MIL-STD-883D-2003	245°C, 5 sec
HOLT	MIL-STD-883D-1005.7	1000 Hrs Bias @125°C
PCT	JESD-22-B, A102	168 Hrs, 100%RH, 121°C
TST	MIL-STD-883D-1011.9	-65°C~150°C, 200 Cycles

Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.	3°C/second max.
Preheat - Temperature Min (T _{min}) - Temperature Max (T _{max}) - Time (min to max) (ts)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: - Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak/Classification Temperature (T _p)	See table 1	See table 2
Time within 5°C of actual Peak Temperature (tp)	10-30 seconds	20-40 seconds
Ramp-down Rate	6°C/second max.	6°C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Note: All temperatures refer to topside of the package. Measured on the body surface.

Table 1. SnPb Eutectic Process – Package Peak Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ ≈350
<2.5 mm	240 +0/-5°C	225 +0/-5°C
≥2.5 mm	225 +0/-5°C	225 +0/-5°C

Table 2. Pb-free Process – Package Classification Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 +0°C*	260 +0°C*	260 +0°C*
1.6 mm – 2.5 mm	260 +0°C*	250 +0°C*	245 +0°C*
≥2.5 mm	250 +0°C*	245 +0°C*	245 +0°C*

*Tolerance: The device manufacturer/supplier **shall** assure process compatibility up to and including the stated classification temperature (this means Peak reflow temperature +0°C. For example 260°C+0°C) at the rated MSL level.

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